

SPIN VALVE TRANSISTOR WITH SELF-PINNED ANTIPARALLEL PINNED LAYER STRUCTURE

ABSTRACT OF THE DISCLOSURE

5 A spin valve transistor includes an emitter, a collector, a base between the emitter and the collector, a spin valve which includes a free layer structure, a self-pinned antiparallel (AP) pinned layer structure and a nonmagnetic spacer layer between the free layer structure and the AP pinned layer structure wherein the base includes at least the free layer structure.

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